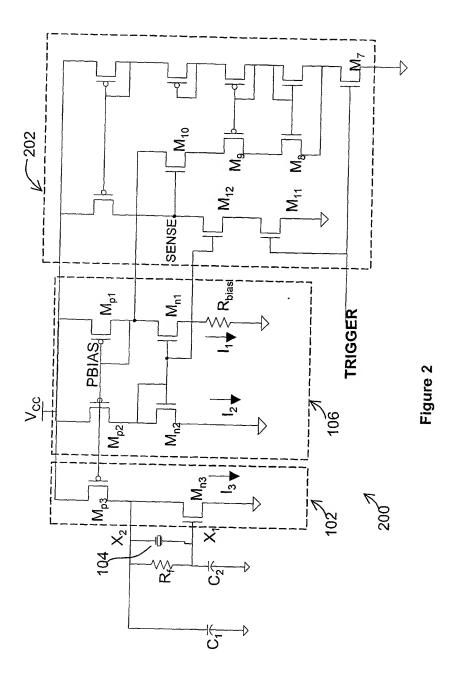


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Applicant(s): Chinnugounder Senthilkumar et al.
LOW POWER OSCILLATOR CIRCUIT

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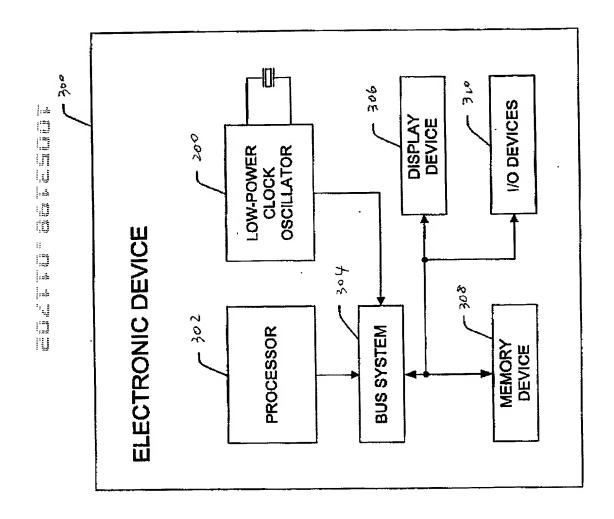


FIG. 3